

A

B

C

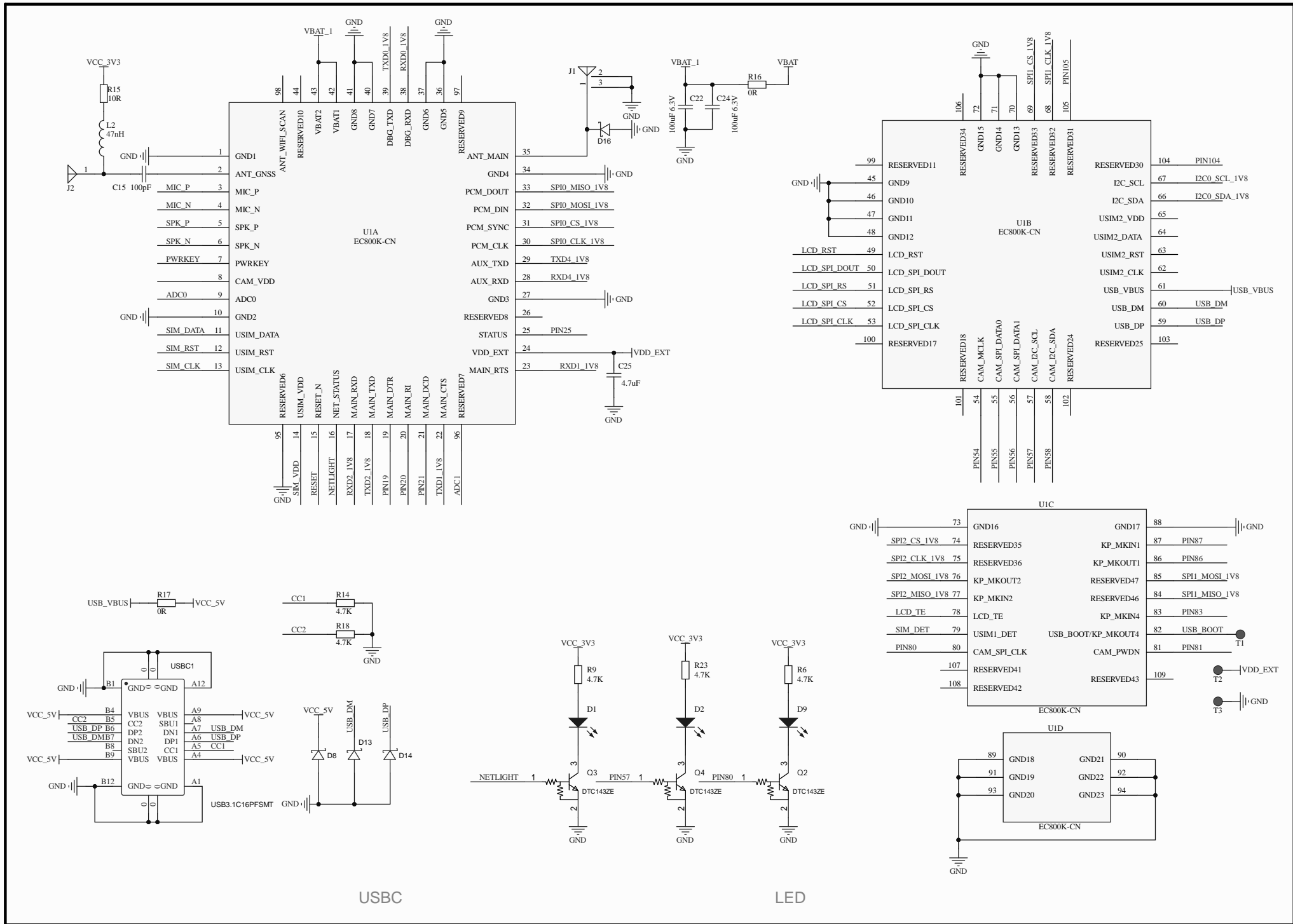
D

A

B

C

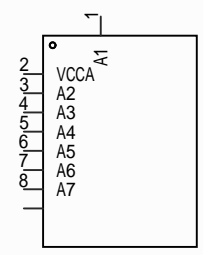
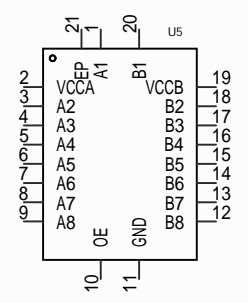
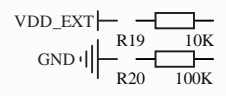
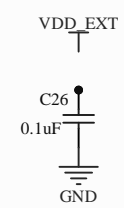
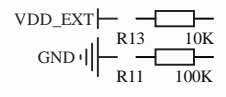
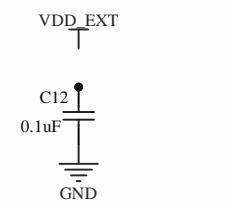
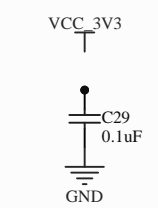
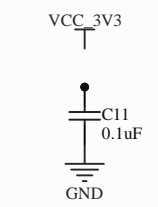
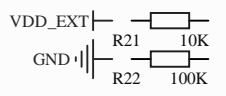
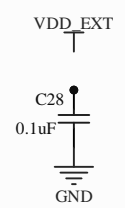
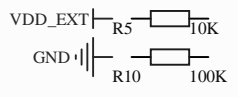
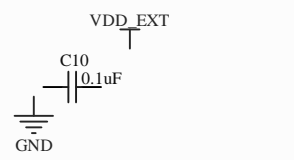
D



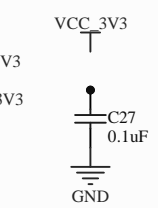
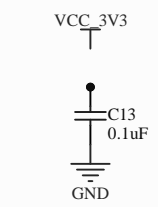
USBC

LED

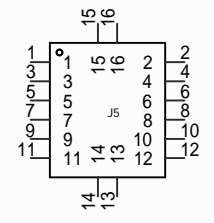
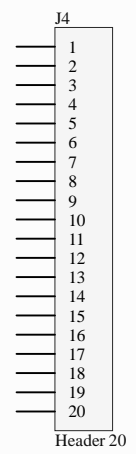
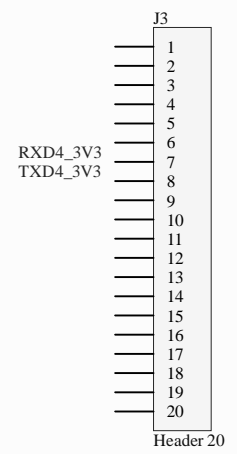
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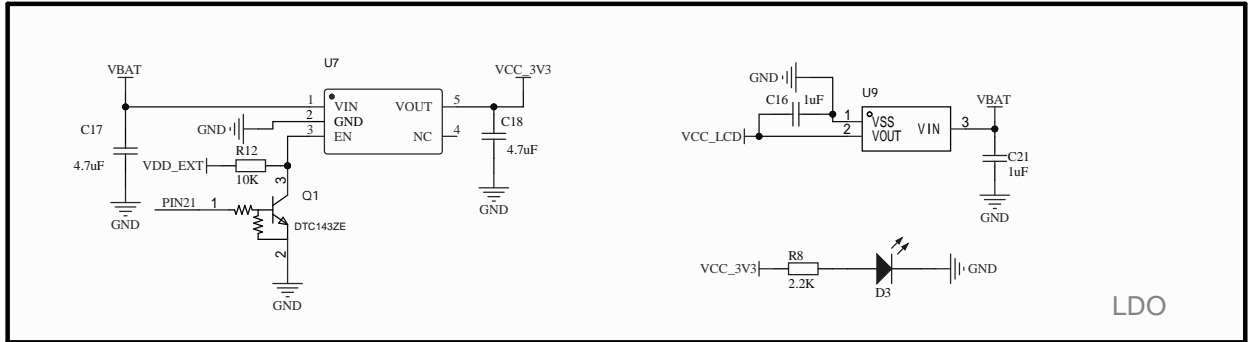


I2C0_SCL_3V3
I2C0_SDA_3V3

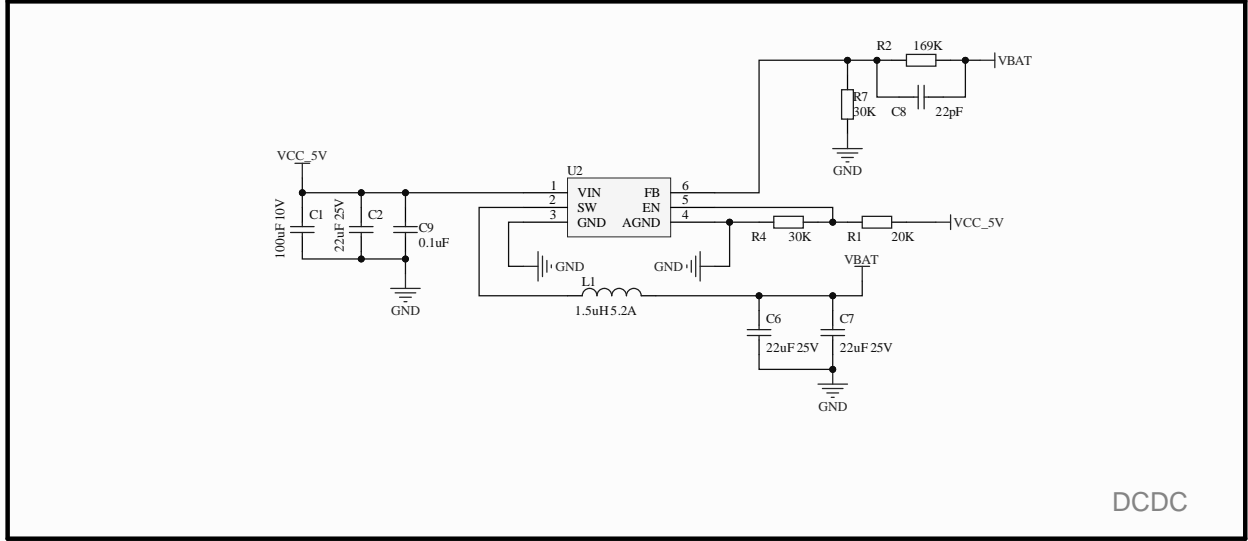


Level Translator





LDO



DCDC

1

2

3

4

5

6

A

A

B

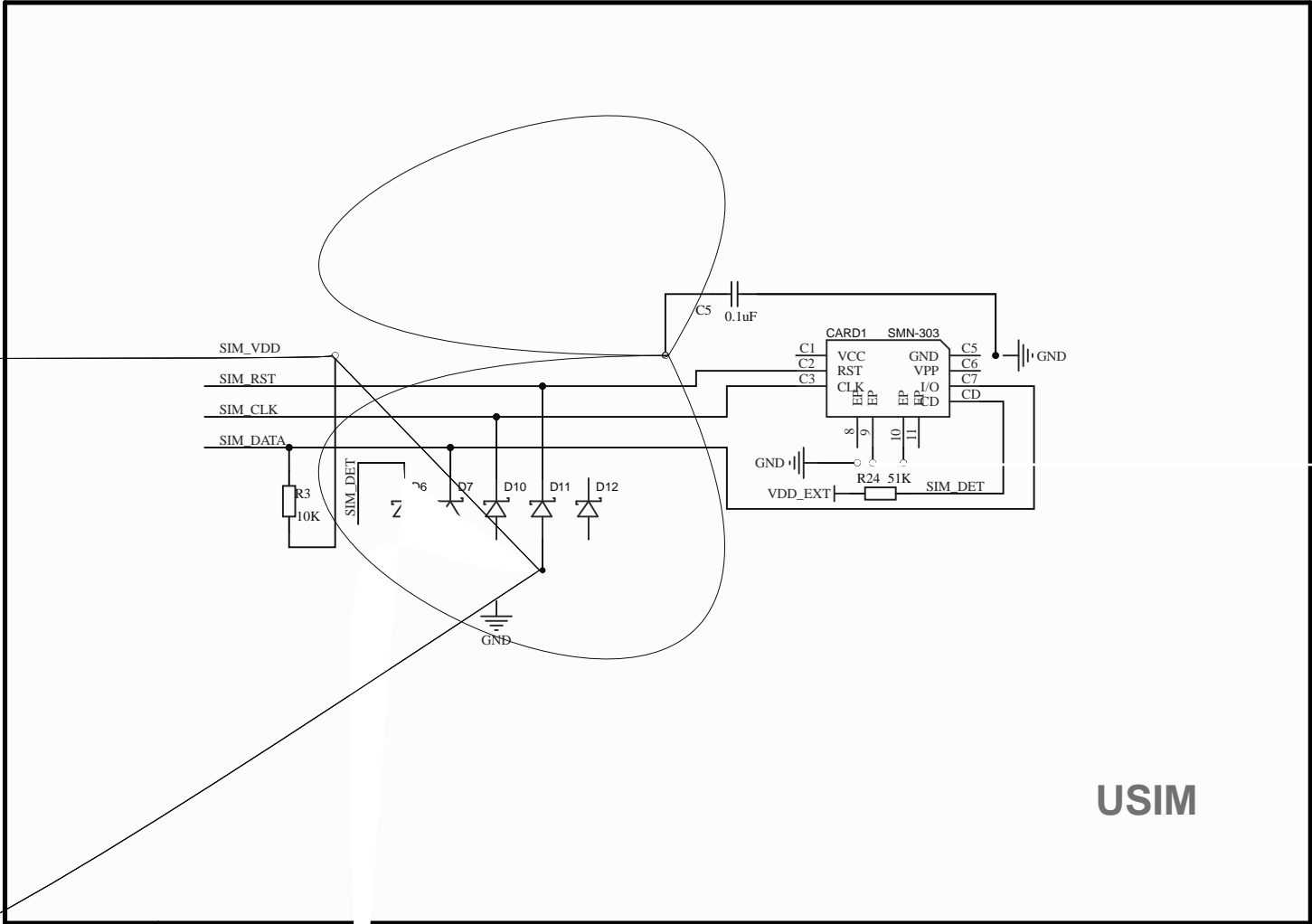
B

C

C

D

D



USIM

1

2

3

4

5

6